

FIG. 1A

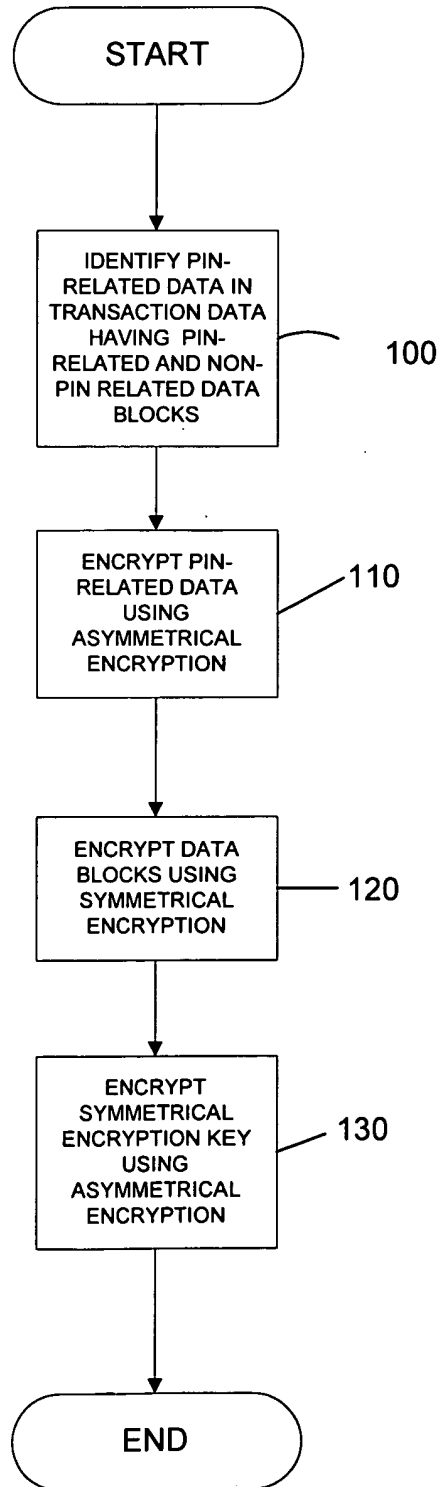


FIG. 1B

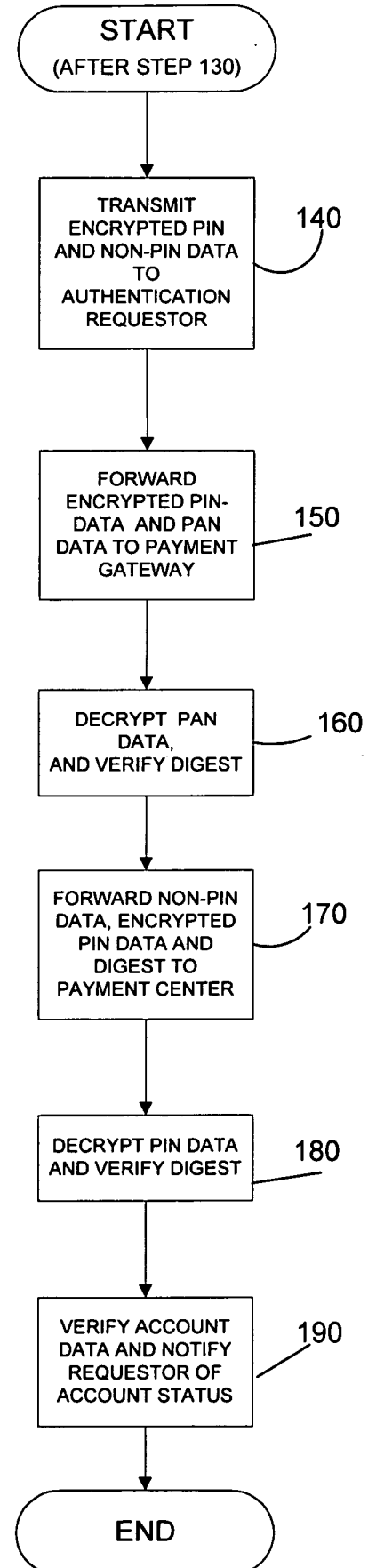


FIG. 2A

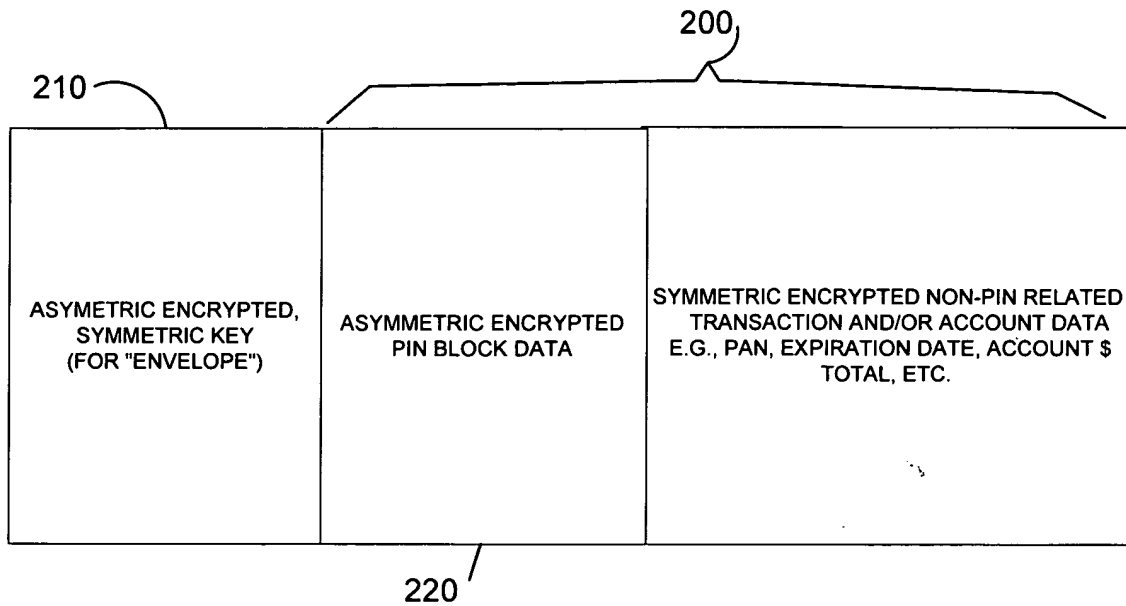
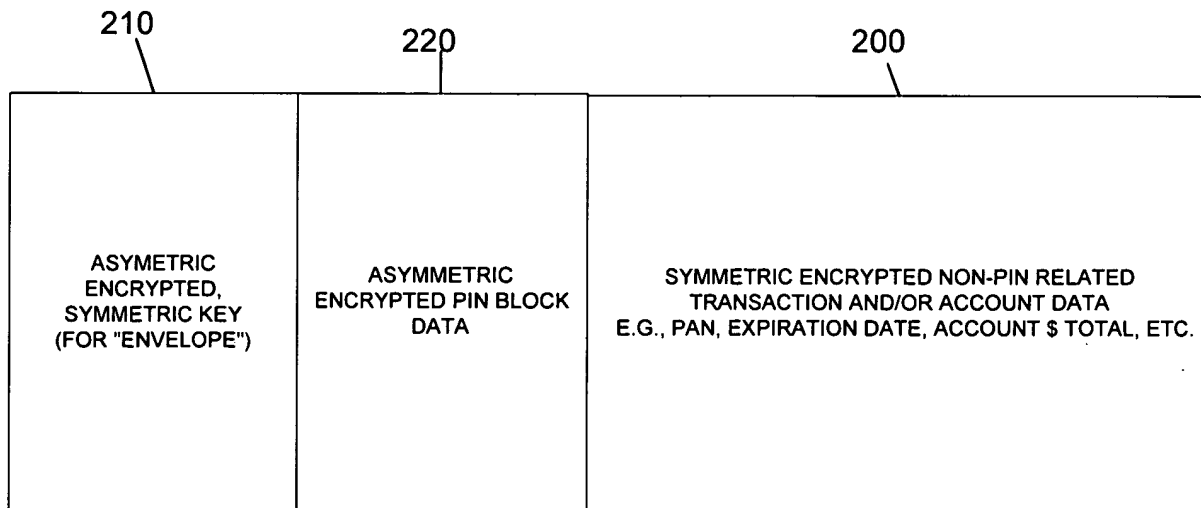


FIG. 2B



# Proposed OAEP-2 PIN Block

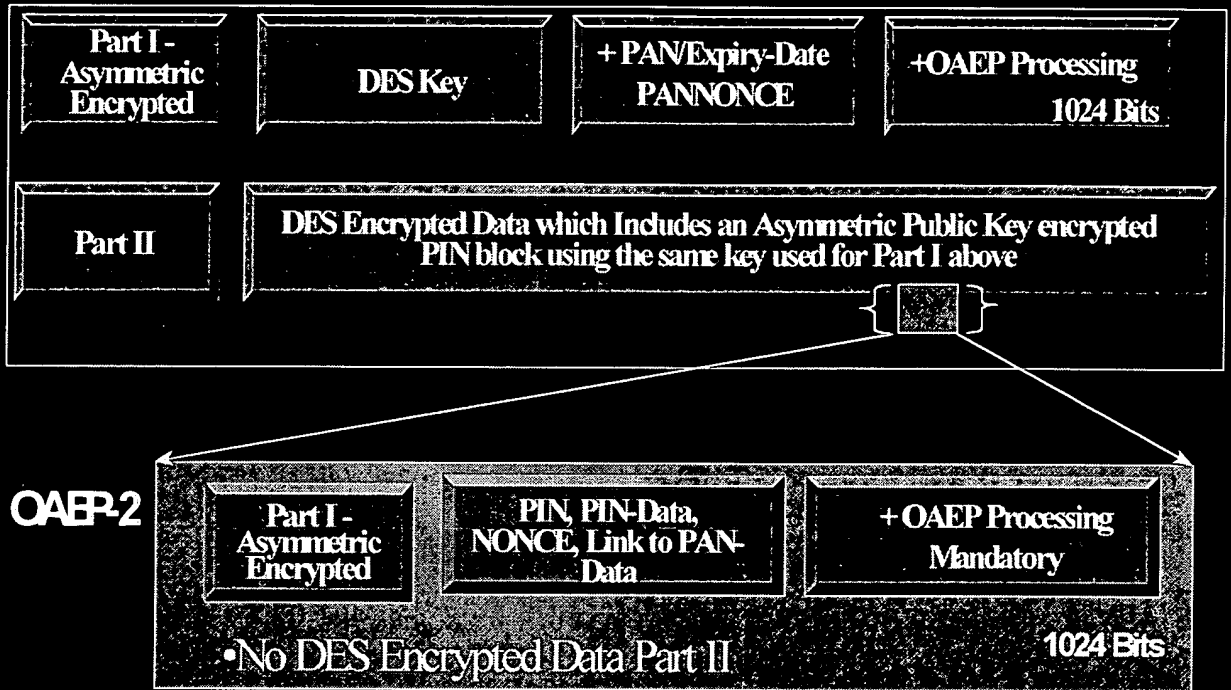


Fig. 20

FIG. 3A

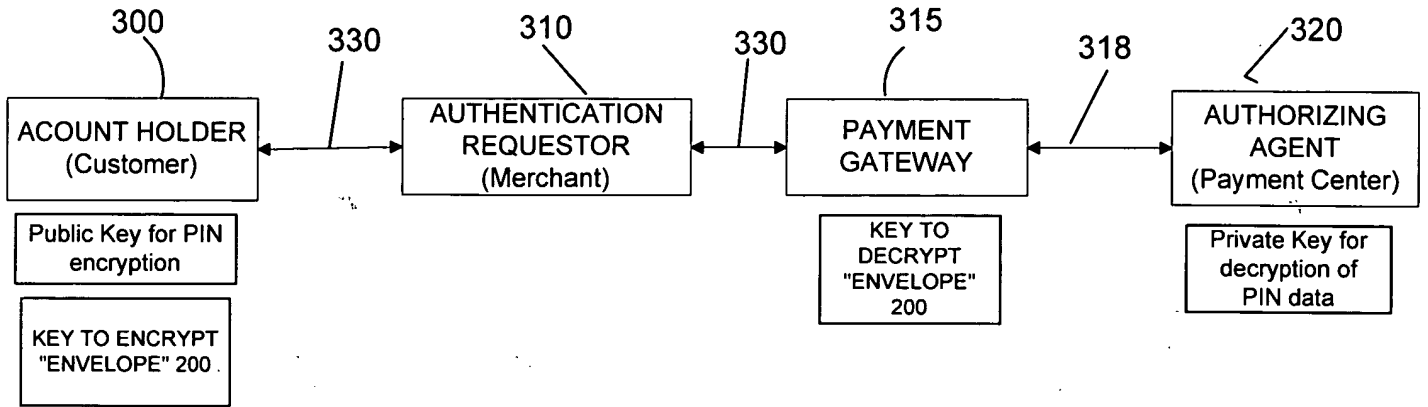


FIG. 3B

